

Bharat Institute of Engineering and Technology

Mangalpally (V), Ibrahimpatnam (M), R.R.Dt -501510

Department of Electronics and Communication Engineering

Time Table for Academic Year 2019-20

Year: IV A

Semester: 1st

Room No: NA-105

DAY	I	II		III	IV	LUNCH	V	VI	VII
	9:20-10:10	10:10-11:00	11:00-11:10	11:10-12:00	12:00-12:50		01:30-02:20	02:20-03:10	03:10-04:00
MON	VLSI	VLSI /MWE LAB					ESD (SPL)	TRAINING	VLSI BRIBGE CLASS
TUE	MWE	EMI	INTERVAL	ESD	VLSI		COMMUNICATION SKILLS TRAINING		CN BRIDGE CLASS
WED	MWE	CN		ESD	VLSI		MINI PROJECT		
THU	VLSI	CN		ESD	MWE		MWE / VLSI LAB		
FRI	EMI	CN		EMI	MWE		TRAINING		MWE BRIBGE CLASS
SAT	EMI	MWE		CN	VLSI		SEMINAR		NPTEL / LIBRARY
Class Incharge:		Counsellor Name					Roll Nos.		Sign
B. SRIDHAR		B.SRIDHAR				16E11A0401-423			
Ph: 9490739244		V.PRADEEP KUMAR				16E11A0425-434,436,437,439-447,17E15A0401			

VLSI	VLSI DESIGN :	V.PRADEEP KUMAR	MWE LAB	B.SRIDHAR
CN	COMPUTER NETWORKS :	Dr.V.RAMBABU	VLSI LAB	V.PRADEEP KUMAR,G.JOY SANGEETH RAJ
ESD	EMBEDDED SYSTEM DESIGN :	G.JOY SANGEETH RAJ	SEMINAR	B. PRASHANTH / G JOY SANGEETH RAJ
EMI	ELECTRONIC MEASUREMENT AND INSTRUMENTATION :	G. KAMALAKER REDDY	MINI PROJE	(B.SRIDHAR
MWE	MICROWAVE ENGINEERING :	B.SRIDHAR	COMMUNICATION SKILLS T	I ZEHRA
SPL-SPECIAL CLASS			TRAINING	C KRANTHI REKHA / K PAVAN KUMAR

TIME TABLE I/C
T V Suresh Kumar

ACADEMIC I/C
G. Joy Sangeeth Raj

ADMIN. I/C
Dr. K. S. Balamurugan

Bharat Institute of Engineering and Technology

Mangalpally (V), Ibrahimpatnam (M), R.R.Dt -501510

Department of Electronics and Communication Engineering

Time Table for Academic Year 2019-20

Year: IV B

Semester: 1st

Room No: NA-114

DAY	I	II		III	IV		V	VI	VII
	9:20-10:10	10:10-11:00	11:00-11:10	11:10-12:00	12:00-12:50		01:30-02:20	02:20-03:10	03:10-04:00
MON	MWE	CN	INTERVAL	EMI	TRAINING	LUNCH	RS (SPL)	VLSI (SPL)	VLSI BRIBGE CLASS
TUE	EMI	CN		RS	MWE		TRAINING		CN BRIDGE CLASS
WED	EMI	VLSI		RS	MWE		MWE / VLSI LAB		
THU	MWE	VLSI		RS	VLSI		COMMUNICATION SKILLS TRAINING		NPTEL
FRI	CN	VLSI / MWE LAB		VLSI / MWE LAB			MINI PROJECT		
SAT	VLSI	CN		SEMINAR			EMI	MWE	LIBRARY
Class Incharge:			Counsellor Name			Roll Nos.			Sign
G.P.RAMACHARYULU			G.P.RAMACHARYULU			16E11A0448-467,470-471			
Ph: 9611777161			B.PRASHANTH			16-472-481,483,484,487-494,17-404,15-462,4C4			

VLSI	VLSI DESIGN :	V.PRADEEP KUMAR	MWE LAB	G.P.RAMACHARYULU /B.PRASANTH
CN	COMPUTER NETWORKS :	B PRASHANTH	VLSI LAB	V.PRADEEP KUMAR / DR.VIKAS MAHESHWARI
RS	RADAR SYSTEMS :	PROF.G.KUMARASWAMY RAO	SEMINAR	DR. K S BALAMURUGAN
EMI	ELECTRONIC MEASUREMENT AND INSTRUMENTATION :	G. KAMALAKER REDDY	MINI PROJE	(B.PRASHANTH / G P RAMA
MWE	MICROWAVE ENGINEERING :	G.P.RAMACHARYULU	COMMUNICATION SKILLS TRAINING :	ARPITHA
SPL-SPECIAL CLASS			TRAINING	C KRANTHI REKHA / K PAVAN KUMAR

TIME TABLE I/C
T V Suresh Kumar

ACADEMIC I/C
G. Joy Sangeeth Raj

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Dr. K. S. Balamurugan

Bharat Institute of Engineering and Technology

Mangalpally (V), Ibrahimpatnam (M), R.R.Dt - 501510

Time Table for Academic Year 2019-20

Year: IV C

Semester: 1st

Room No: NA-402

DAY	I	II		III	IV	LUNCH	V	VI	VII	
	9:20-10:10	10:10-11:00	11:00-11:10	11:10-12:00	12:00-12:50		01:30-02:20	02:20-03:10	03:10-04:00	
MON	CN	VLSI	INTERVAL	ESD	TRAINING		MWE (SPL)	SEMINAR	VLSI BRIBGE CLASS	
TUE	CN	VLSI / MWE LAB		VLSI / MWE LAB			ESD	EMI (SPL)	LIB	
WED	EMI	ESD (SPL)		VLSI			MWE	COMMUNICATION SKILLS TRAINING		MWE BRIBGE CLASS
THU	VLSI	MWE		TRAINING			MINI PROJECT			
FRI	MWE	CN		EMI			VLSI	MWE / VLSI LAB		
SAT	EMI	SEMINAR		CN			MWE	VLSI	ESD	
Class Incharge:		Counsellor Name			Roll Nos.				Sign	
Dr.PAPIYA DUTTA		Dr.PAPIYA DUTTA M PRASHANTH			16E11A0495,496,497-499,4A1-4B4,4B6-4B8 16E11A04B9-4C2,4C4-4C7,4C9-4E1,17E15A0402					
Ph: 9827587253										

VLSI	VLSI DESIGN :	R.BASAVA RAJU	MWE LAB	B.SRIDHAR,
CN	COMPUTER NETWORKS :	Dr.PAPIYA DUTTA	VLSI LAB	Dr.NEERAJ KUMAR MISHRA,R.BASAVA RAJU
ESD	EMBEDDED SYSTEM DESIGN :	DR. NAVEEN RATHEE	SEMINAR	Dr.VIKAS MAHESHWAR
EMI	ELECTRONIC MEASUREMENT AND INSTRUMENTATION :	SOUMYA K PARADDI	MINI PROJECT	Dr.NEERAJ KUMAR MISHRA
MWE	MICROWAVE ENGINEERING :	B.SRIDHAR	COMMUNICATION SKILLS TRAINING :	BATOOOL
SPL-SPECIAL CLASS			TRAINING	C KRANTHI REKHA / K PAVAN KUMAR

TIME TABLE I/C
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Dr. K. S. Balamurugan

Bharat Institute of Engineering and Technology

Mangalpally (V), Ibrahimpatnam (M), R.R.Dt - 501510

Department of Electronics and Communication Engineering

Time Table for Academic Year 2019-20

Year: IV D

Semester: 1st

Room No: NA- 403

YEAR IV - B		SEMESTER - I		Room No. 171-403					
DAY	I	II		III	IV	V	VI	VII	
	9:20-10:10	10:10-11:00	11:00-11:10	11:10-12:00	12:00-12:50	01:30-02:20	02:20-03:10	03:10-04:00	
MON	EMI	CN	INTERVAL	VLSI	MWE	COMMUNICATION SKILLS TRAINING		VLSI BRIBGE CLASS	
TUE	VLSI	MWE		CN	ESD	MWE / VLSI LAB			
WED	TRAINING			SEMINAR	VLSI	VLSI (SPL)	ESD (SPL)	MWE BRIBGE CLASS	
THU	EMI	VLSI / MWE LAB		VLSI / MWE LAB			ESD	MWE (SPL)	CN BRIDGE CLASS
FRI	MWE	MINI PROJECT		MINI PROJECT			TRAINING	EMI (SPL)	SEMINAR
SAT	CN	CN		MWE	EMI	ESD	VLSI	LIBRARY	
Class Incharge:		Counsellor Name			Roll Nos.				Sign
R BASAVA RAJU		R.BASAVA RAJU SURYA SURENDRAN			16E11A04E2-4E4,4E6-4E9,4F1-4G4 16E11A04G5-4H0,4H2-4H1,4I3-4I5,4I7-4I9,17-403				
Ph: 9703266688									

VLSI	VLSI DESIGN :	R.BASAVA RAJU	MWE LAB	G.P.RAMACHARYULU
CN	COMPUTER NETWORKS :	Dr.PAPIYA DUTTA	VLSI LAB	B. PRASHANTH/ R.BASAVA RAJU
ESD	EMBEDDED SYSTEM DESIGN :	DR. NAVEEN RATHEE	SEMINAR	Dr.PAPIYA DUTTA
EMI	ELECTRONIC MEASUREMENT AND INSTRUMENTATION :	SOUMYA K PARADDI	MINI PROJECT	DR. NAVEEN RATHEE
MWE	MICROWAVE ENGINEERING :	G.P.RAMACHARYULU	COMMUNICATION SKILLS TRAINING :	SHIVA KUMAR RAJA
SPL-SPECIAL CLASS			TRAINING	C KRANTHI REKHA / K PAVAN KUMAR

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